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IN THE CLAIMS:

The text of all pending claims, (including withdrawn claims) is set forth below. Cancelled and not entered claims are indicated with claim number and status only. The claims as listed below show added text with underlining and deleted text with ~~strikethrough~~. The status of each claim is indicated with one of (original), (currently amended), (cancelled), (withdrawn), (new), (previously presented), or (not entered).

Please CANCEL claims 1-6, 16-19, and 31-36 without prejudice or disclaimer in accordance with the following:

1-36. (CANCELLED)

37. (PREVIOUSLY PRESENTED) A flat panel display, comprising:

a gate line, a data line and a power supply line; and

a plurality of pixels connected to the lines,

wherein each of the pixels comprises first and second thin film transistors each comprising a bias supply layer, a channel region, a source region, and a drain region in an active layer; and voltage is applied to the channel regions of the thin film transistors to discharge hot carriers; and the source, channel, and drain regions of each of the thin film transistors extend parallel to the gate line, and the channel region and the bias supply layer of each of the thin film transistors extend parallel to the data or power supply lines.

38. (PREVIOUSLY PRESENTED) The flat panel display of claim 37, wherein the bias supply layer and the source region of the first thin film transistor are connected to the data line.

39. (PREVIOUSLY PRESENTED) The flat panel display of claim 37, wherein the bias supply layer and the source region of the second thin film transistor are connected to the power supply line.

40. (PREVIOUSLY PRESENTED) The flat panel display of claim 37, further comprising a capacitor having a first electrode and a second electrode, and the second thin film transistor comprising a gate electrode, wherein the drain region of the first thin film transistor and

the gate electrode of the second thin film transistor are connected to the first electrode of the capacitor, and the second electrode of the capacitor is connected to the power supply line.